

**Dual N-Channel Enhancement-Mode MOSFET (20V,15A)****PRODUCT SUMMARY**

V_{DS}	I_D	$R_{DS(on)}$ (m Ω) Typ
20V	15A	5.5@ $V_{GS}=4.5V$, $I_D=10A$
		5.9 @ $V_{GS}=4.0V$, $I_D=5A$
		7.7@ $V_{GS}=2.5V$, $I_D=2.5A$

Features

- Super high density cell design for extremely low RDS(ON)
- Exceptional on-resistance and maximum DC current capability
- ESD Rating:2000V HBM
- Lead (Pb) -free and halogen-free

Applications

Power Management in Notebook Computer Portable Equipment and Battery Powered One Cell Li-ion Battery Pack.

**Absolute Maximum Ratings** ($T_A=25^{\circ}C$, unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	± 12	V
I_D	Drain Current (Continuous)@ $T_A=25^{\circ}C$	15	A
	Drain Current (Continuous)@ $T_A=75^{\circ}C$	12	A
I_{DM}	Drain Current (Pulsed) ^a	60	A
P_D	Total Power Dissipation @ $T_A=25^{\circ}C$	1.9	W
	Total Power Dissipation @ $T_A=75^{\circ}C$	1.2	W
T_J, T_{stg}	Operating Junction and Storage Temperature Range	-55 to +150	$^{\circ}C$
R_{QJA}	Thermal Resistance Junction to Ambient (PCB mounted) ^b	35	$^{\circ}C/W$

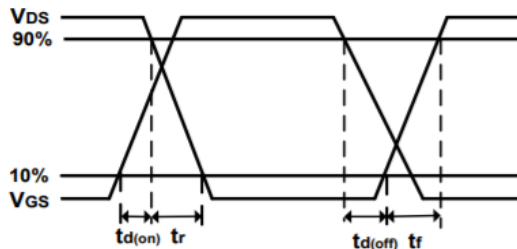
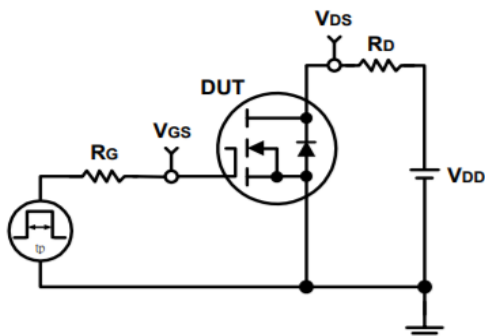
a: Repetitive Rating: Pulse width limited by the maximum junction temperature.

b: 1-in² 2oz Cu PCB board

Electrical Characteristics (T_A=25°C, unless otherwise noted)

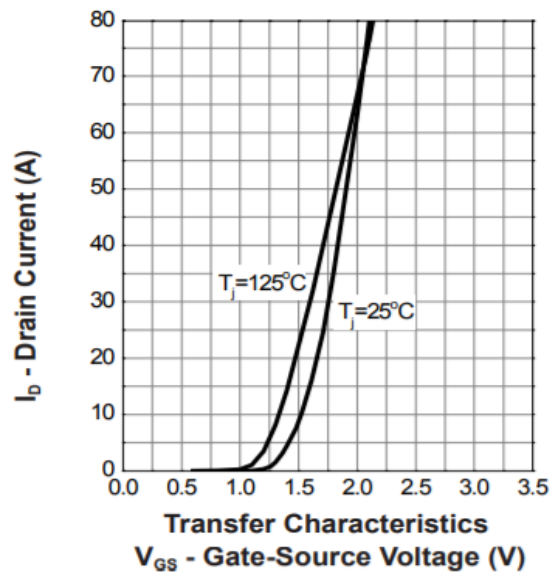
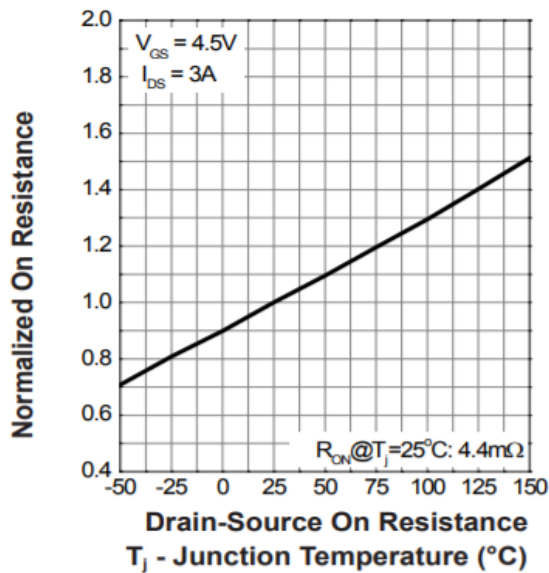
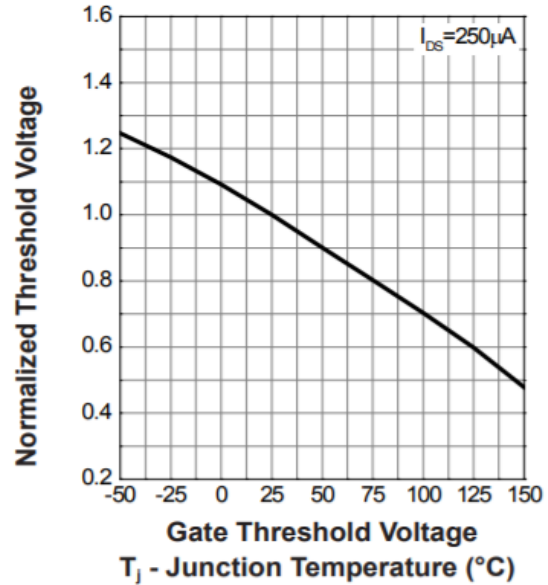
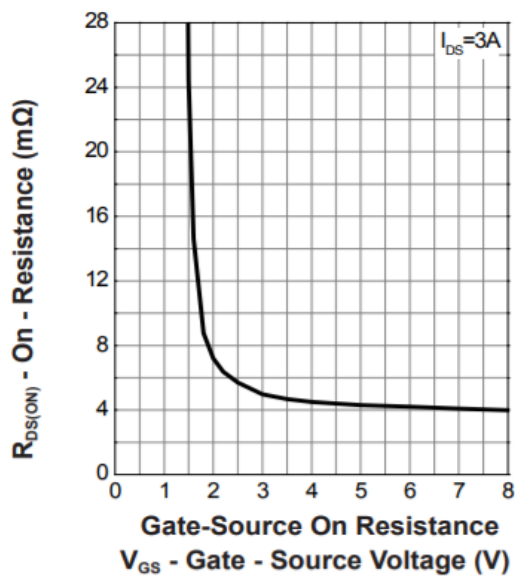
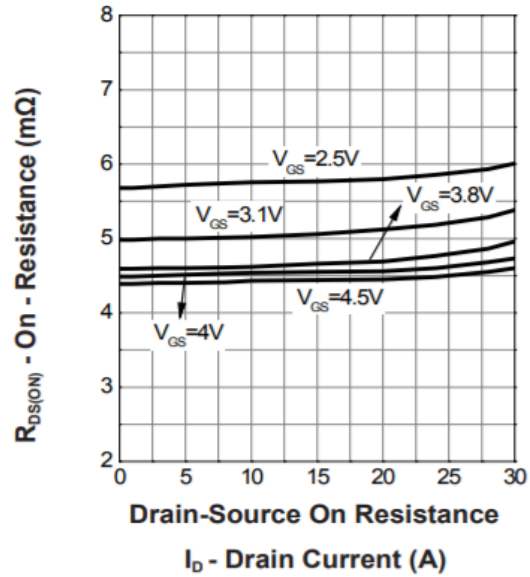
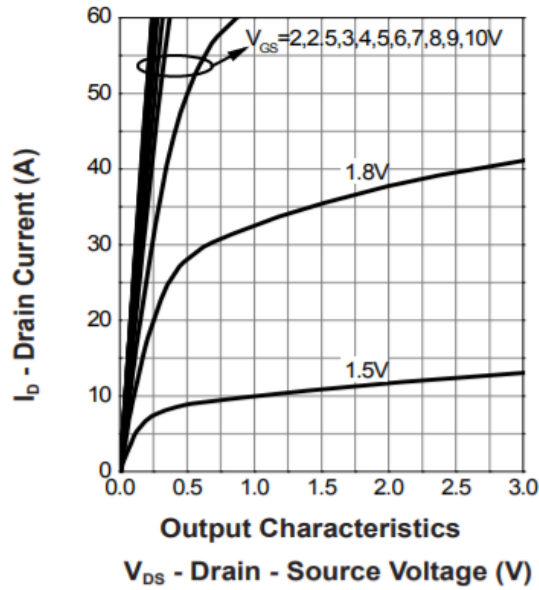
Symbol	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
• Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	20	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =20V, V _{GS} =0V	-	-	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±12V, V _{DS} =0V	-	-	±10	μA
• On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	0.5	0.7	1.0	V
R _{DS(on)}	Drain-Source On-State Resistance	V _{GS} =4.5V, I _D =10A	3.3	4.4	5.5	mΩ
		V _{GS} =4.0V, I _D =5A	3.4	4.6	5.9	
		V _{GS} =2.5V, I _D =2.5A	3.7	5.5	7.7	
g _{FS}	Forward Transconductance	V _{DS} =10V, I _D =5A	-	18.5	-	s
• Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =10V, V _{GS} =0V, f=1MHz	-	1580	2200	PF
C _{oss}	Output Capacitance		-	260	-	
C _{rss}	Reverse Transfer Capacitance		-	170	-	
• Switching Characteristics						
Q _g	Total Gate Charge	V _{DS} =10V, I _D =3A, V _{GS} =10V	-	17	-	nC
Q _{gs}	Gate-Source Charge		-	1.3	-	
Q _{gd}	Gate-Drain Charge		-	2.4	-	
t _{d(on)}	Turn-on Delay Time	V _{DD} =10V, R _L =10Ω, I _D =3A, V _{GEN} =10V, R _G =6Ω	-	10.1	-	nS
t _r	Turn-on Rise Time		-	16	-	
t _{d(off)}	Turn-off Delay Time		-	850	-	
t _f	Turn-off Fall Time		-	354	-	
• Drain-Source Diode Characteristics						
V _{SD}	Drain-Source Diode Forward	V _{GS} =0V, I _S =10A	-	0.75	1.2	V

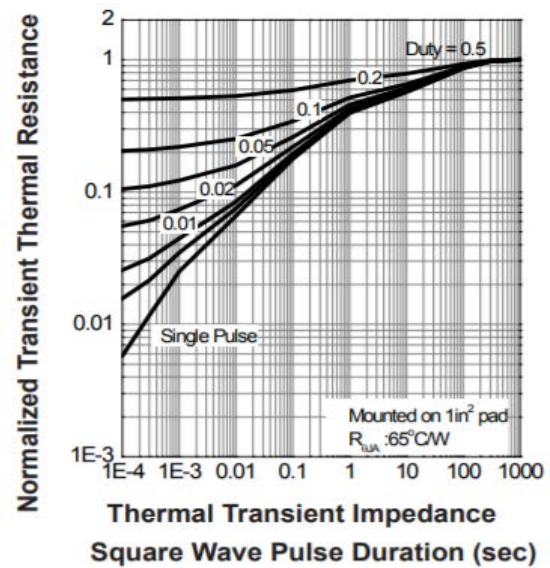
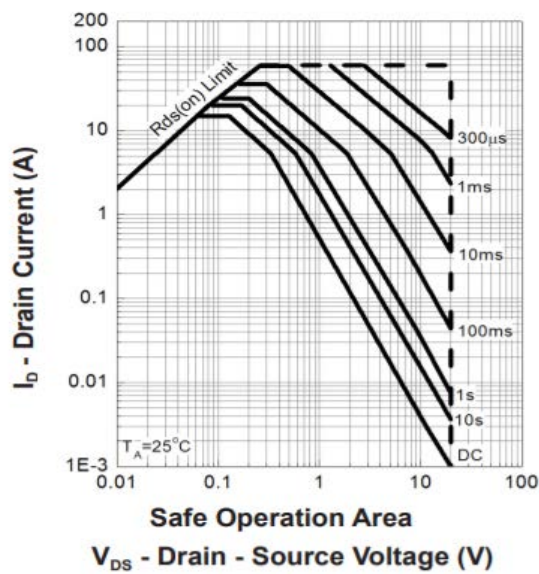
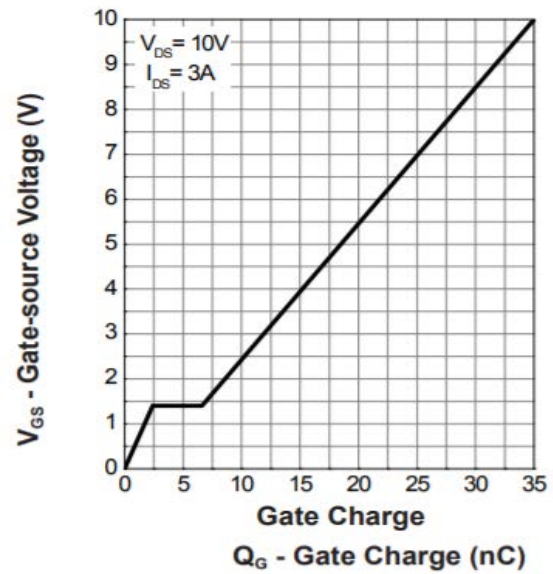
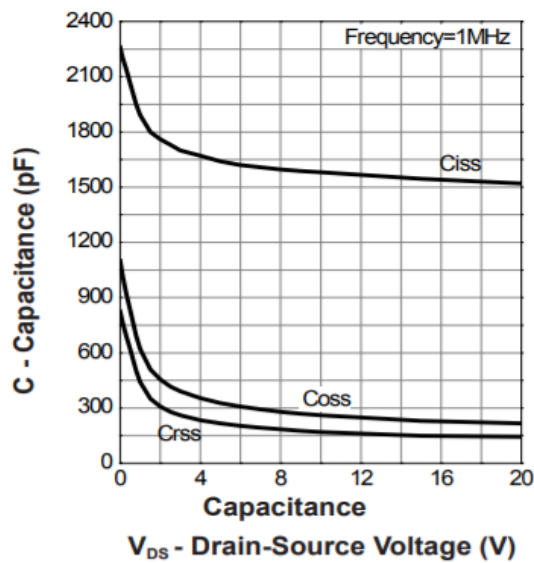
Note: Pulse Test: Pulse Width≤300us, Duty Cycle≤2%



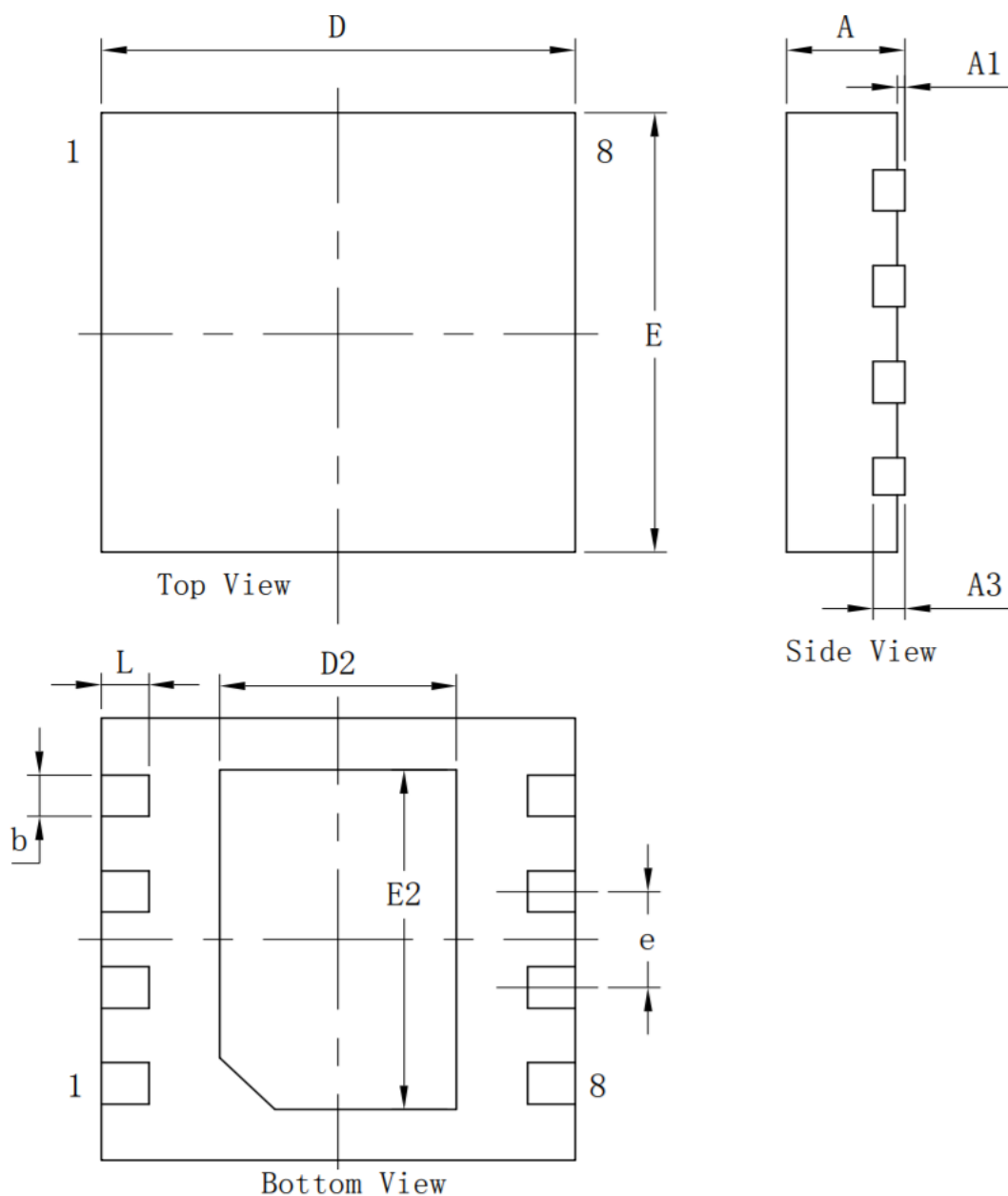
Switching Time Test Circuit and Waveforms

Typical Characteristics Curves (Ta=25°C, unless otherwise note)





DFN3*3-8L PACKAGE OUTLINE DIMENSIONS (1)



Symbol	Dimensions (unit : mm)		
	Min	TYP	Max
A	0.70	0.75	0.8
A1			0.1
A3	0.203REF		
b	0.23	0.28	0.33
D	2.90	3.00	3.1
E	2.90	3.00	3.1
D2	1.40	1.50	1.6
E2	2.20	2.30	2.4
e	0.65		
L	0.25	0.30	0.35